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Introducing the Verilog HDL in a brief format, this book presents a selected set of the changes the popular hardware underwent in its first revision—emerging as IEEE Std 1364-2001 or Verilog-2001. It addresses the main features that support the design of combinational and sequential logic, and emphasizes synthesizable models, with a limited discussion of the theoretical framework for synthesis.

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Verilog — 2001 by Sutherland, Stuart (ebook)

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This edition is based on IEEE Verilog 2001 Standard. It includes explanations of all the new features introduced in this version of the language, with examples. (iii) Digital Design: With an Introduction to the Verilog HDL, VHDL and System Verilog. This book teaches the basic concepts of digital design in a clear, accessible manner.

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Furthermore, Verilog 2001 features were mentioned as appropriate. The examples are diverse and exhaustive, making this a perfect reference guide for people who learn best by example. Also, appendices provide invaluable reference materials for language features and primitives.

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The first implementation of Verilog-A soon followed: a version from Cadence that ran on their Spectre circuit simulator. As more implementations of Verilog-A became available, the group defining the a- log and mixed-signal extensions to Verilog continued their work, releasing the definition of Verilog-AMS in 2000.

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